FEATURES

- 5A Onboard Switch (LT1074)
- Operates Up to 60V Input
- 100kHz Switching Frequency
- Greatly Improved Dynamic Behavior
- Available in Low Cost 5 and 7-Lead Packages
- Only 8.5mA Quiescent Current
- Programmable Current Limit
- Micropower Shutdown Mode

APPLICATIONS

- Buck Converter with Output Voltage Range of 2.5V to 50V
- Tapped-Inductor Buck Converter with 10A Output at 5V
- Positive-to-Negative Converter
- Negative Boost Converter
- Multiple Output Buck Converter

DESCRIPTION

The LT®1074 is a 5A (LT1076 is rated at 2A) monolithic bipolar switching regulator which requires only a few external parts for normal operation. The power switch, all oscillator and control circuitry, and all current limit components, are included on the chip. The topology is a classic positive “buck” configuration but several design innovations allow this device to be used as a positive-to-negative converter, a negative boost converter, and as a flyback converter. The switch output is specified to swing 40V below ground, allowing the LT1074 to drive a tapped-inductor in the buck mode with output currents up to 10A.

The LT1074 uses a true analog multiplier in the feedback loop. This makes the device respond nearly instantaneously to input voltage fluctuations and makes loop gain independent of input voltage. As a result, dynamic behavior of the regulator is significantly improved over previous designs.

On-chip pulse by pulse current limiting makes the LT1074 nearly bust-proof for output overloads or shorts. The input voltage range as a buck converter is 8V to 60V, but a self-boot feature allows input voltages as low as 5V in the inverting and boost configurations.

The LT1074 is available in low cost TO-220 or DD packages with frequency pre-set at 100kHz and current limit at 6.5A (LT1076 = 2.6A). A 7-pin TO-220 package is also available which allows current limit to be adjusted down to zero. In addition, full micropower shutdown can be programmed. See Application Note 44 for design details.

A fixed 5V output, 2A version is also available. See LT1076-5.

\[ T \] LTC and LT are registered trademarks of Linear Technology Corporation.

TYPICAL APPLICATION

Basic Positive Buck Converter

Buck Converter Efficiency

<table>
<thead>
<tr>
<th>LT1074</th>
<th>VOUT = 12V, VIN = 20V</th>
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<tbody>
<tr>
<td>VOUT</td>
<td>EFFICIENCY (%)</td>
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<tr>
<td>0</td>
<td>100</td>
</tr>
<tr>
<td>1</td>
<td>90</td>
</tr>
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<td>2</td>
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<table>
<thead>
<tr>
<th>OUTPUT LOAD CURRENT (A)</th>
<th>EFFICIENCY (%)</th>
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<tr>
<td>5</td>
<td>50</td>
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<tr>
<td>6</td>
<td>40</td>
</tr>
</tbody>
</table>
**LT1074/LT1076**

### Absolute Maximum Ratings (Note 1)

**Input Voltage**
- \(45\text{V}\) (LT1074/ LT1076)
- \(64\text{V}\) (LT1074HV/LT1076HV)

**Switch Voltage with Respect to Input Voltage**
- \(64\text{V}\) (LT1074/ LT1076)
- \(75\text{V}\) (LT1074HV/LT1076HV)

**Switch Voltage with Respect to Ground Pin (\(V_{SW}\) Negative)**
- \(35\text{V}\) (LT1074/LT1076)
- \(45\text{V}\) (LT1074HV/LT1076HV)

**Feedback Pin Voltage**
- \(-2\text{V}, +10\text{V}\)

**Shutdown Pin Voltage (Not to Exceed \(V_{IN}\))**
- \(40\text{V}\)

**ILIM Pin Voltage (Forced)**
- \(5.5\text{V}\)

**Maximum Operating Ambient Temperature Range**
- **Commercial**: \(0^\circ\text{C} \text{ to } 70^\circ\text{C}\)
- **Industrial**: \(-40^\circ\text{C} \text{ to } 85^\circ\text{C}\)
- **Military (OBSOLETE)**: \(-55^\circ\text{C} \text{ to } 125^\circ\text{C}\)

**Maximum Operating Junction Temperature Range**
- **Commercial**: \(0^\circ\text{C} \text{ to } 125^\circ\text{C}\)
- **Industrial**: \(-40^\circ\text{C} \text{ to } 125^\circ\text{C}\)
- **Military (OBSOLETE)**: \(-55^\circ\text{C} \text{ to } 150^\circ\text{C}\)

**Maximum Storage Temperature**
- \(-65^\circ\text{C} \text{ to } 150^\circ\text{C}\)

**Lead Temperature (Soldering, 10 sec)**
- \(300^\circ\text{C}\)

**Q Package**
- 5-Lead Plastic DIP
- 7-Lead Plastic DIP

**K Package**
- 4-Lead TO-3 Metal Can

**T Package**
- 5-Lead Plastic TO-220

**T7 Package**
- 7-Lead Plastic TO-220

Consult LTC Marketing for parts specified with wider operating temperature ranges.

*Assumes package is soldered to 0.5 IN² of 1 oz. copper over internal ground plane or over back side plane.

Consult LTC Marketing for parts specified with wider operating temperature ranges.

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**PACKAGE/ORDER INFORMATION**

***LT1076CQ***
- **LT1076IQ**

***LT1076CR***
- **LT1076IR**
- **LT1076HVCR**
- **LT1076HVIR**

***LT1074CT7***
- **LT1074HVCT7**
- **LT1074IT7**
- **LT1074HVIT7**
- **LT1076CT7**
- **LT1076HVCT7**

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**Order Part Number**

**LT1074CK**
- **LT1074HVCK**
- **LT1074MK**
- **LT1074HVMK**
- **LT1076CK**
- **LT1076HVCK**
- **LT1076MK**
- **LT1076HVMK**

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**OBSCALTE PACKAGE**

Consider the T5 Package for Alternate Source.
## ELECTRICAL CHARACTERISTICS

The * denotes the specifications which apply over the full operating temperature range, otherwise specifications are at $T_A = 25^\circ C$, $T_J = 25^\circ C$, $V_{IN} = 25V$, unless otherwise noted.

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<tr>
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<th>TYP</th>
<th>MAX</th>
<th>UNITS</th>
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<td>2.1</td>
<td>2.3</td>
<td>V</td>
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<td>$I_{SW} = 1A$, $T_J &lt; 0^\circ C$</td>
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<td>2.5</td>
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<td>LT1076 $I_{SW} = 0.5A$</td>
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<td>$I_{SW} = 2A$</td>
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<td></td>
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<tr>
<td>Switch “Off” Leakage</td>
<td>LT1074 $V_{IN} \leq 25V$, $V_{SW} = 0$</td>
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<td>300</td>
<td>µA</td>
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<td>$V_{IN} = V_{MAX}$, $V_{SW} = 0$ (Note 8)</td>
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<td>LT1076 $V_{IN} = 25V$, $V_{SW} = 0$</td>
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<td>$V_{IN} = V_{MAX}$, $V_{SW} = 0$ (Note 8)</td>
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<td>Supply Current (Note 3)</td>
<td>$V_{FB} = 2.5V$, $V_{IN} \leq 40V$</td>
<td>8.5</td>
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<td>$40V &lt; V_{IN} &lt; 60V$</td>
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<td>$V_{SHUT} = 0.1V$ (Device Shutdown) (Note 9)</td>
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<td>300</td>
<td>µA</td>
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<td>Minimum Supply Voltage</td>
<td>Normal Mode</td>
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<td>8</td>
<td>V</td>
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<td>Startup Mode (Note 4)</td>
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<tr>
<td>Switch Current Limit (Note 5)</td>
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<td>6.5</td>
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<td>A</td>
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<td>$R_{LIM} = 10k$ (Note 6)</td>
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<td>$R_{LIM} = 10k$ (Note 6)</td>
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<td>$R_{LIM} = 7k$ (Note 6)</td>
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<td>Maximum Duty Cycle</td>
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<td>85</td>
<td>90</td>
<td>%</td>
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<td>Switching Frequency</td>
<td>$T_J \leq 125^\circ C$</td>
<td>90</td>
<td>100</td>
<td>110</td>
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<td>$T_J &gt; 125^\circ C$</td>
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<td>120</td>
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<td>$V_{FB} = 0V$ through $2k\Omega$ (Note 5)</td>
<td>85</td>
<td>125</td>
<td>kHz</td>
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<td>Switching Frequency Line Regulation</td>
<td>$8V \leq V_{IN} \leq V_{MAX}$ (Note 8)</td>
<td>0.03</td>
<td>0.1</td>
<td>%/V</td>
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<tr>
<td>Error Amplifier Voltage Gain (Note 7)</td>
<td>$1V \leq V_{C} \leq 4V$</td>
<td>2000</td>
<td>V/V</td>
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<td>Error Amplifier Transconductance</td>
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<td>3700</td>
<td>5000</td>
<td>8000</td>
<td>µmhos</td>
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<td>Error Amplifier Source and Sink Current</td>
<td>Source ($V_{FB} = 2V$)</td>
<td>100</td>
<td>140</td>
<td>225</td>
<td>µA</td>
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<td>Sink ($V_{FB} = 2.5V$)</td>
<td>0.7</td>
<td>1</td>
<td>1.6</td>
<td>mA</td>
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<tr>
<td>Feedback Pin Bias Current</td>
<td>$V_{FB} = V_{REF}$</td>
<td>0.5</td>
<td>2</td>
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<tr>
<td>Reference Voltage</td>
<td>$V_{C} = 2V$</td>
<td>2.155</td>
<td>2.21</td>
<td>2.265</td>
<td>V</td>
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<td>Reference Voltage Tolerance</td>
<td>$V_{REF}$ (Nominal) = 2.21V</td>
<td>±0.5</td>
<td>±1.5</td>
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<td>All Conditions of Input Voltage, Output Voltage, Temperature and Load Current</td>
<td>±1</td>
<td>±2.5</td>
<td>%</td>
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<td>Reference Voltage Line Regulation</td>
<td>$8V \leq V_{IN} \leq V_{MAX}$ (Note 8)</td>
<td>0.005</td>
<td>0.02</td>
<td>%/V</td>
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<td>$V_{C}$ Voltage at 0% Duty Cycle</td>
<td>Over Temperature</td>
<td>1.5</td>
<td>–4</td>
<td>mV/°C</td>
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<td>Multiplier Reference Voltage</td>
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<td>Shutdown Pin Current</td>
<td>$V_{SH} = 5V$</td>
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<td>10</td>
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<td>$V_{SH} \leq V_{THRESHOLD}$ (≤2.5V)</td>
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<td>Shutdown Thresholds</td>
<td>Switch Duty Cycle = 0</td>
<td>2.2</td>
<td>2.45</td>
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<td>V</td>
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<td>Fully Shut Down</td>
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<td>0.6</td>
<td>V</td>
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<td>Thermal Resistance Junction to Case</td>
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<td>LT1076</td>
<td>4.0</td>
<td>°C/W</td>
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</tbody>
</table>
ELECTRICAL CHARACTERISTICS

Note 1: Absolute Maximum Ratings are those values beyond which the life of a device may be impaired.

Note 2: To calculate maximum switch “on” voltage at currents between low and high conditions, a linear interpolation may be used.

Note 3: A feedback pin voltage (VFB) of 2.5V forces the VC pin to its low clamp level and the switch duty cycle to zero. This approximates the zero load condition where duty cycle approaches zero.

Note 4: Total voltage from VIN pin to ground pin must be ≥ 8V after start-up for proper regulation.

Note 5: Switch frequency is internally scaled down when the feedback pin voltage is less than 1.3V to avoid extremely short switch on times. During testing, VFB is adjusted to give a minimum switch on time of 1µs.

Note 6: I_{LIM} \approx \frac{R_{LIM} - 1k}{2k} (LT1074), I_{LIM} \approx \frac{R_{LIM} - 1k}{5.5k} (LT1076).

Note 7: Switch to input voltage limitation must also be observed.

Note 8: V_{MAX} = 40V for the LT1074/76 and 60V for the LT1074HV/76HV.

Note 9: Does not include switch leakage.
A switch cycle in the LT1074 is initiated by the oscillator setting the R/S latch. The pulse that sets the latch also locks out the switch via gate G1. The effective width of this pulse is approximately 700ns, which sets the maximum switch duty cycle to approximately 93% at 100kHz switching frequency. The switch is turned off by comparator C1, which resets the latch. C1 has a sawtooth waveform as one input and the output of an analog multiplier as the other input. The multiplier output is the product of an internal reference voltage, and the output of the error amplifier, A1, divided by the regulator input voltage. In standard buck regulators, this means that the output voltage of A1 required to keep a constant regulated output is independent of regulator input voltage. This greatly improves line transient response, and makes loop gain independent of input voltage. The error amplifier is a transconductance type with a $G_M$ at null of approximately 5000μmho. Slew current going positive is 140μA, while negative slew current is about 1.1mA. This asymmetry helps prevent overshoot on start-up. Overall loop frequency compensation is accomplished with a series RC network from $V_C$ to ground.

Switch current is continuously monitored by C2, which resets the R/S latch to turn the switch off if an overcurrent condition occurs. The time required for detection and switch turn off is approximately 600ns. So minimum switch “on” time in current limit is 600ns. Under dead shorted output conditions, switch duty cycle may have to be as low as 2% to maintain control of output current. This would require switch on time of 200ns at 100kHz switching frequency, so frequency is reduced at very low output voltages by feeding the FB signal into the oscillator and creating a linear frequency downshift when the FB signal drops below 1.3V. Current trip level is set by the voltage on the $I_{LIM}$ pin which is driven by an internal 320μA current source. When this pin is left open, it self-clamps at about 4.5V and sets current limit at 6.5A for the LT1074 and 2.6A for the LT1076. In the 7-pin package an external resistor can be connected from the $I_{LIM}$ pin to ground to set a lower current limit. A capacitor in parallel with this resistor will soft-start the current limit. A slight offset in C2 guarantees that when the $I_{LIM}$ pin is pulled to within 200mV of ground, C2 output will stay high and force switch duty cycle to zero.

The “Shutdown” pin is used to force switch duty cycle to zero by pulling the $I_{LIM}$ pin low, or to completely shut down the regulator. Threshold for the former is approximately 2.35V, and for complete shutdown, approximately 0.3V. Total supply current in shutdown is about 150μA. A 10μA pull-up current forces the shutdown pin high when left open. A capacitor can be used to generate delayed start-up. A resistor divider will program “undervoltage lockout” if the divider voltage is set at 2.35V when the input is at the desired trip point.

The switch used in the LT1074 is a Darlington NPN (single NPN for LT1076) driven by a saturated PNP. Special patented circuitry is used to drive the PNP on and off very quickly even from the saturation state. This particular switch arrangement has no “isolation tubs” connected to the switch output, which can therefore swing to 40V below ground.
PIN DESCRIPTIONS

**V\text{IN PIN}**
The V\text{IN} pin is both the supply voltage for internal control circuitry and one end of the high current switch. It is important, especially at low input voltages, that this pin be bypassed with a low ESR, and low inductance capacitor to prevent transient steps or spikes from causing erratic operation. At full switch current of 5A, the switching transients at the regulator input can get very large as shown in Figure 1. Place the input capacitor very close to the regulator and connect it with wide traces to avoid extra inductance. Use radial lead capacitors.

**Ground Pin**
It might seem unusual to describe a ground pin, but in the case of regulators, the ground pin must be connected properly to ensure good load regulation. The internal reference voltage is referenced to the ground pin; so any error in ground pin voltage will be multiplied at the output;

\[ \Delta V_{\text{OUT}} = \frac{(\Delta V_{\text{GND}})(V_{\text{OUT}})}{2.21} \]

To ensure good load regulation, the ground pin must be connected directly to the proper output node, so that no high currents flow in this path. The output divider resistor should also be connected to this low current connection line as shown in Figure 2.

**Feedback Pin**
The feedback pin is the inverting input of an error amplifier which controls the regulator output by adjusting duty cycle. The noninverting input is internally connected to a trimmed 2.21V reference. Input bias current is typically 0.5\( \mu \)A when the error amplifier is balanced (I\text{OUT} = 0). The error amplifier has asymmetrical G\text{M} for large input signals to reduce startup overshoot. This makes the amplifier more sensitive to large ripple voltages at the feedback pin. 100mVP-p ripple at the feedback pin will create a 14mV offset in the amplifier, equivalent to a 0.7% output voltage shift. To avoid output errors, output ripple (P-P) should be less than 4% of DC output voltage at the point where the output divider is connected.

See the “Error Amplifier” section for more details.

**Frequency Shifting at the Feedback Pin**
The error amplifier feedback pin (FB) is used to downshift the oscillator frequency when the regulator output voltage is low. This is done to guarantee that output short-circuit...
PIN DESCRIPTIONS

Current is well controlled even when switch duty cycle must be extremely low. Theoretical switch “on” time for a buck converter in continuous mode is:

\[ t_{\text{ON}} = \frac{V_{\text{OUT}} + V_D}{V_{\text{IN}} \cdot f} \]

- \( V_D \) = Catch diode forward voltage ( = 0.5V)
- \( f \) = Switching frequency

At \( f = 100\text{kHz} \), \( t_{\text{ON}} \) must drop to 0.2\( \mu\text{s} \) when \( V_{\text{IN}} = 25\text{V} \) and the output is shorted (\( V_{\text{OUT}} = 0\text{V} \)). In current limit, the LT1074 can reduce \( t_{\text{ON}} \) to a minimum value of \( \approx 0.6\mu\text{s} \), much too long to control current correctly for \( V_{\text{OUT}} = 0 \). To correct this problem, switching frequency is lowered from 100kHz to 20kHz as the FB pin drops from 1.3V to 0.5V. This is accomplished by the circuitry shown in Figure 3.

Q1 is off when the output is regulating (\( V_{\text{FB}} = 2.21\text{V} \)). As the output is pulled down by an overload, \( V_{\text{FB}} \) will eventually reach 1.3V, turning on Q1. As the output continues to drop, Q1 current increases proportionately and lowers the frequency of the oscillator. Frequency shifting starts when the output is \( \approx 60\% \) of normal value, and is down to its minimum value of \( \approx 20\text{kHz} \) when the output is \( \approx 20\% \) of normal value. The rate at which frequency is shifted is determined by both the internal 3k resistor R3 and the external divider resistors. For this reason, R2 should not be increased to more than 4k\( \Omega \), if the LT1074 will be subjected to the simultaneous conditions of high input voltage and output short-circuit.

SHUTDOWN PIN

The shutdown pin is used for undervoltage lockout, micropower shutdown, soft-start, delayed start, or as a general purpose on/off control of the regulator output. It controls switching action by pulling the \( I_{\text{LIM}} \) pin low, which forces the switch to a continuous “off” state. Full micropower shutdown is initiated when the shutdown pin drops below 0.3V.

The V/I characteristics of the shutdown pin are shown in Figure 4. For voltages between 2.5V and \( = V_{\text{IN}} \), a current of 10\( \mu\text{A} \) flows out of the shutdown pin. This current increases to \( \approx 25\mu\text{A} \) as the shutdown pin moves through the 2.35V threshold. The current increases further to \( \approx 30\mu\text{A} \) at the 0.3V threshold, then drops to \( \approx 15\mu\text{A} \) as the shutdown voltage fall below 0.3V. The 10\( \mu\text{A} \) current source is included to pull the shutdown pin to its high or default state when left open. It also provides a convenient pull-up for delayed start applications with a capacitor on the shutdown pin.

When activated, the typical collector current of Q1 in Figure 5, is \( \approx 2\text{mA} \). A soft-start capacitor on the \( I_{\text{LIM}} \) pin will delay regulator shutdown in response to \( C_1 \), by \( = (5\text{V})(C_{\text{LIM}})/2\text{mA} \). Soft-start after full micropower shutdown is ensured by coupling \( C_2 \) to Q1.
**PIN DESCRIPTIONS**

**Figure 5. Shutdown Circuitry**

**Undervoltage Lockout**

Undervoltage lockout point is set by R1 and R2 in Figure 6. To avoid errors due to the 10μA shutdown pin current, R2 is usually set at 5k, and R1 is found from:

\[
R_1 = R_2 \left( \frac{V_{TP} - V_{SH}}{V_{SH}} \right)
\]

- **V_{TP}** = Desired undervoltage lockout voltage
- **V_{SH}** = Threshold for lockout on the shutdown pin = 2.45V

If quiescent supply current is critical, R2 may be increased up to 15kΩ, but the denominator in the formula for R2 should replace **V_{SH}** with **V_{SH} - (10\mu A)(R2)**.

**Figure 6. Undervoltage Lockout**

Hysteresis in undervoltage lockout may be accomplished by connecting a resistor (R3) from the **I_{LIM}** pin to the shutdown pin as shown in Figure 7. D1 prevents the shutdown divider from altering current limit.

**Figure 7. Adding Hysteresis**

\[
\text{Trip Point} = V_{TP} = 2.35V \left( 1 + \frac{R_1}{R_2} \right)
\]

If R3 is added, the lower trip point (**V_{IN}** descending) will be the same. The upper trip point (**V_{UTP}**) will be:

\[
V_{UTP} = V_{SH} \left( \frac{R_1}{R_2} + \frac{R_1}{R_3} \right) - 0.8V \left( \frac{R_1}{R_3} \right)
\]

If R1 and R2 are chosen, R3 is given by:

\[
R_3 = \frac{\left( V_{SH} - 0.8V \right)(R_1)}{V_{UTP} - V_{SH} \left( 1 + \frac{R_1}{R_2} \right)}
\]

**Example:** An undervoltage lockout is required such that the output will not start until **V_{IN}** = 20V, but will continue to operate until **V_{IN}** drops to 15V. Let R2 = 2.32k.

\[
R_1 = \left( 2.34k \right) \left( \frac{15V - 2.35V}{2.35V} \right) = 12.5k
\]

\[
R_3 = \left( \frac{2.35 - 0.8}{20 - 2.35} \right) \left( \frac{12.5}{2.32} \right) = 3.9k
\]
PIN DESCRIPTIONS

ILIM PIN
The ILIM pin is used to reduce current limit below the preset value of 6.5A. The equivalent circuit for this pin is shown in Figure 8.

![Figure 8. ILIM Pin Circuit](LT1074-4512)

When ILIM is left open, the voltage at Q1 base clamps at 5V through D2. Internal current limit is determined by the current through Q1. If an external resistor is connected between ILIM and ground, the voltage at Q1 base can be reduced for lower current limit. The resistor will have a voltage across it equal to (320μA)(R), limited to ~5V when clamped by D2. Resistance required for a given current limit is:

\[
R_{\text{LIM}} = I_{\text{LIM}}(2kΩ) + 1kΩ \quad \text{(LT1074)}
\]

\[
R_{\text{LIM}} = I_{\text{LIM}}(5.5kΩ) + 1kΩ \quad \text{(LT1076)}
\]

As an example, a 3A current limit would require 3A(2k) + 1k = 7kΩ for the LT1074. The accuracy of these formulas is ±25% for 2A ≤ I_{\text{LIM}} ≤ 5A (LT1074) and 7A ≤ I_{\text{LIM}} ≤ 1.8A (LT1076), so I_{\text{LIM}} should be set at least 25% above the peak switch current required.

Foldback current limiting can be easily implemented by adding a resistor from the output to the ILIM pin as shown in Figure 9. This allows full desired current limit (with or without R_{\text{LIM}}) when the output is regulating, but reduces current limit under short-circuit conditions. A typical value for R_{FB} is 5kΩ, but this may be adjusted up or down to set the amount of foldback. D2 prevents the output voltage from forcing current back into the ILIM pin. To calculate a value for R_{FB}, first calculate R_{LIM}, the R_{FB}:

\[
R_{\text{FB}} = \frac{(I_{\text{SC}} - 0.44 \times RL)}{0.5 \times (R_{\text{L}} - 1kΩ) - I_{\text{SC}}} \times (R_{\text{L}} \text{ in } kΩ)
\]

*Change 0.44 to 0.16, and 0.5 to 0.18 for LT1076.

Example: I_{\text{LIM}} = 4A, ISC = 1.5A, R_{\text{LIM}} = (4)(2k) + 1k = 9k

\[
R_{\text{FB}} = \frac{(1.5 - 0.44)(9kΩ)}{0.5(9k - 1k) - 1.5} \approx 3.8kΩ
\]

![Figure 9. Foldback Current Limit](LT1074-4512)

Error Amplifier
The error amplifier in Figure 10 is a single stage design with added inverters to allow the output to swing above and below the common mode input voltage. One side of the amplifier is tied to a trimmed internal reference voltage of 2.21V. The other input is brought out as the FB (feedback) pin. This amplifier has a G_M (voltage “in” to current “out”) transfer function of ~5000μmhos. Voltage gain is determined by multiplying G_M times the total equivalent output loading, consisting of the output resistance of Q4 and Q6 in parallel with the series RC external frequency compensation network. At DC, the external RC is ignored, and with a parallel output impedance for Q4 and Q6 of 400kΩ, voltage gain is ~2000. At frequencies above a few hertz, voltage gain is determined by the external compensation, R_C and C_C.
**FIGURE 10. ERROR AMPLIFIER**

The error amplifier has asymmetrical peak output current. Q3 and Q4 current mirrors are unity-gain, but the Q6 mirror has a gain of 1.8 at output null and a gain of 8 when the FB pin is high (Q1 current = 0). This results in a maximum positive output current of 140\(\mu\)A and a maximum negative (sink) output current of \(\pm 1.1\text{mA}\). The asymmetry is deliberate—it results in much less regulator output overshoot during rapid start-up or following the release of an output overload. Amplifier offset is kept low by area scaling Q1 and Q2 at 1.8:1.

Amplifier swing is limited by the internal 5.8V supply for positive outputs and by D1 and D2 when the output goes low. Low clamp voltage is approximately one diode drop (=0.7V – 2mV/°C).

Note that both the FB pin and the VC pin have other internal connections. Refer to the frequency shifting and synchronizing discussions.

**PIN DESCRIPTIONS**

The error amplifier has asymmetrical peak output current. Q3 and Q4 current mirrors are unity-gain, but the Q6 mirror has a gain of 1.8 at output null and a gain of 8 when the FB pin is high (Q1 current = 0). This results in a maximum positive output current of 140\(\mu\)A and a maximum negative (sink) output current of \(\pm 1.1\text{mA}\). The asymmetry is deliberate—it results in much less regulator output overshoot during rapid start-up or following the release of an output overload. Amplifier offset is kept low by area scaling Q1 and Q2 at 1.8:1.

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TYPICAL APPLICATIONS

Tapped-Inductor Buck Converter

Positive-to-Negative Converter with 5V Output

* PULSE ENGINEERING #PE#65282
** MOTOROLA MBR2030CTL
† IF INPUT VOLTAGE IS BELOW 20V, MAXIMUM OUTPUT CURRENT WILL BE REDUCED. SEE AN44

---

* = 1% FILM RESISTORS
D1 = MOTOROLA-MBR745
C1 = NICHICON-UPL1C221MRH6
C2 = NICHICON-UPL1A102MRH6
L1 = COILTRONICS-CTX25-5-52

† LOWER REVERSE VOLTAGE RATING MAY BE USED FOR LOWER INPUT VOLTAGES. LOWER CURRENT RATING IS ALLOWED FOR LOWER OUTPUT CURRENT. SEE AN44.

** R1, R2, AND C4 ARE USED FOR LOOP FREQUENCY COMPENSATION WITH LOW INPUT VOLTAGE, BUT R1 AND R2 MUST BE INCLUDED IN THE CALCULATION FOR OUTPUT VOLTAGE DIVIDER VALUES. FOR HIGHER OUTPUT VOLTAGES, INCREASE R1, R2, AND R3 PROPORTIONATELY. FOR INPUT VOLTAGE > 10V, R1, R2, AND C4 CAN BE ELIMINATED, AND COMPENSATION IS DONE TOTALLY ON THE VC PIN.
R3 = VOUT / 2.37 (KΩ)
R1 = (R3) (1.86)
R2 = (R3) (3.65)

** MAXIMUM OUTPUT CURRENT OF 1A IS DETERMINED BY MINIMUM INPUT VOLTAGE OF 4.5V. HIGHER MINIMUM INPUT VOLTAGE WILL ALLOW MUCH HIGHER OUTPUT CURRENTS. SEE AN44.
K Package
4-Lead TO-3 Metal Can
(Reference LTC DWG # 05-08-1311)

Q Package
5-Lead Plastic DD Pak
(Reference LTC DWG # 05-08-1461)
R Package
7-Lead Plastic DD Pak
(Reference LTC DWG # 05-08-1462)

T Package
5-Lead Plastic TO-220 (Standard)
(Reference LTC DWG # 05-08-1421)
**TYPICAL APPLICATION**

Negative Boost Converter

![Diagram of LT1074/LT1076 negative boost converter](image)

- **T7 Package**
  - 7-Lead Plastic TO-220 (Standard)
  - Reference LTC DWG # 05-08-1422

**PACKAGE DESCRIPTION**

**T7 Package**

- 7-Lead Plastic TO-220 (Standard)
- Reference LTC DWG # 05-08-1422

**RELATED PARTS**

<table>
<thead>
<tr>
<th>PART NUMBER</th>
<th>DESCRIPTION</th>
<th>COMMENTS</th>
</tr>
</thead>
<tbody>
<tr>
<td>LT1375/LT1376</td>
<td>1.5A, 500kHz Step-Down Switching Regulators</td>
<td>VIN Up to 25V, IOUT Up to 1.25A, SO-8</td>
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<tr>
<td>LT1374/LT1374HV</td>
<td>4.5A, 500kHz Step-Down Switching Regulators</td>
<td>VIN Up to 25V (32V for HV), IOUT Up to 4.25A, SO-8/DD</td>
</tr>
<tr>
<td>LT1370</td>
<td>6A, 500kHz High Efficiency Switching Regulator</td>
<td>VIN Up to 25V, 6A/42V Internal Switch, 7-Lead DD/TO-220</td>
</tr>
<tr>
<td>LT1676</td>
<td>Wide Input Range, High Efficiency Step-Down Regulator</td>
<td>VIN from 7.4V to 60V, IOUT Up to 0.5A, SO-8</td>
</tr>
<tr>
<td>LT1339</td>
<td>High Power Synchronous DC/DC Controller</td>
<td>VIN Up to 60V, IOUT Up to 50A, Current Mode</td>
</tr>
<tr>
<td>LT1765</td>
<td>3A, 1.25MHz, Step-Down Regulator</td>
<td>VIN = 3V to 25V, VREF = 1.2V, TSSOP-16E, SO8 Package</td>
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</tbody>
</table>

**Notes:**
- MBR735
- IOUT (MAX) = 1A to 3A depending on input voltage. See AN44

**Dimensions:**

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<tr>
<th>Part</th>
<th>DIA</th>
<th>BSC</th>
<th>Thickness</th>
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<tbody>
<tr>
<td>0.095 – 0.115</td>
<td>0.330 – 0.370 (8.382 – 9.398)</td>
<td></td>
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<tr>
<td>0.135 – 0.165</td>
<td>0.023 – 0.033 (0.584 – 0.842)</td>
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<tr>
<td>0.260 – 0.320</td>
<td>0.013 – 0.023 (0.330 – 0.584)</td>
<td></td>
<td></td>
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<tr>
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<td>0.023 – 0.033 (0.584 – 0.842)</td>
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</tr>
<tr>
<td>0.023</td>
<td>0.050 (1.27)</td>
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</table>

**Electrical Characteristics:**

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Value</th>
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<tbody>
<tr>
<td>R1</td>
<td>12.7k</td>
</tr>
<tr>
<td>VIN</td>
<td>25V</td>
</tr>
<tr>
<td>VFB</td>
<td>1.2V</td>
</tr>
<tr>
<td>VGND</td>
<td>0V</td>
</tr>
<tr>
<td>R2</td>
<td>2.21k</td>
</tr>
<tr>
<td>C1</td>
<td>1uF</td>
</tr>
<tr>
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<td>2.2uF</td>
</tr>
<tr>
<td>C3</td>
<td>20uF</td>
</tr>
<tr>
<td>L1</td>
<td>2.2uH</td>
</tr>
<tr>
<td>D1</td>
<td></td>
</tr>
<tr>
<td>C3</td>
<td>10uF</td>
</tr>
<tr>
<td>C4</td>
<td>10uF</td>
</tr>
<tr>
<td>R3</td>
<td>750Ω</td>
</tr>
</tbody>
</table>

**TYP (MEASURED AT THE SEATING PLANE)**

- 0.155 – 0.195
- 0.152 – 0.202
- 0.260 – 0.320
- 0.135 – 0.165
- 0.460 – 0.500
- 0.026 – 0.036 (0.660 – 0.914)
- 0.026 – 0.036 (0.660 – 0.914)
- 0.290 – 0.415 (9.006 – 10.541)
- 0.460 – 0.500 (11.684 – 12.700)
- 0.147 – 0.155 (3.724 – 3.937)
- 0.220 – 0.270 (5.842 – 6.858)
- 0.570 – 0.620 (14.478 – 15.748)
- 0.320 – 0.570 (8.382 – 9.398)
- 0.060 – 0.090 (1.524 – 2.286)
- 0.045 – 0.055 (1.143 – 1.397)
- 0.095 – 0.115 (2.413 – 2.921)
- 0.013 – 0.023 (0.330 – 0.584)
- 0.026 – 0.036 (0.660 – 0.914)
- 0.050 (1.27)