

Revised Application Notes and Design Tips

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<p>INT-936 The Do's and Don'ts of Using MOS-Gated Transistors</p> <p style="text-align: center;"><i>(Comprised of: AN-936)</i></p> <p style="text-align: center;">Be Mindful of the Reverse Blocking Characteristics of the Device</p> <p style="text-align: center;">Be Careful When Handling and Testing Power <i>HEXFETs</i></p> <p style="text-align: center;">Beware of Unexpected Gate-to-Source Voltage Spikes</p> <p style="text-align: center;">Beware of Drain or Collector Voltage Spikes Induced by Switching</p> <p style="text-align: center;">Do Not Exceed the Peak Current Rating</p> <p style="text-align: center;">Stay Within the Thermal Limits of the Device</p> <p style="text-align: center;">Pay Attention to Circuit Layout</p> <p style="text-align: center;">Be Careful When Using the Integral Body-Drain Diode</p> <p style="text-align: center;">Be On Your Guard When Comparing Current Ratings</p>	936	7	May-97
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