FAIRCHILD SEMICONDUCTOR®

January 2002

IRFP460A

20A, 500V, 0.22 Ohm, N-Channel SMPS Power MOSFET

Applications

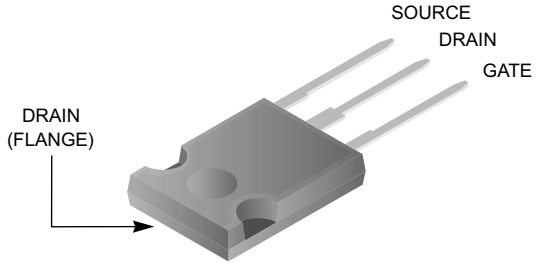
- Switch Mode Power Supplies (SMPS)
- Uninterruptable Power Supply
- High Speed Power Switching

Features

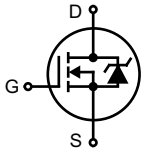
- Low Gate Charge Q_g results in Simple Drive Requirement
- Improved Gate, Avalanche and Dynamic dv/dt Ruggedness
- Improved $r_{DS(ON)}$
- Reduced Miller Capacitance

Package

JEDEC TO-247



Symbol



Absolute Maximum Ratings $T_A = 25^oC$ unless otherwise noted

Symbol	Parameter	Ratings	Units
V_{DSS}	Drain to Source Voltage	500	V
V_{GS}	Gate to Source Voltage	±30	V
I_D	Drain Current Continuous ($T_C = 25^oC$, $V_{GS} = 10V$)	20	A
	Continuous ($T_C = 100^oC$, $V_{GS} = 10V$)	13	A
	Pulsed[1]	80	A
P_D	Power dissipation Derate above 25^oC	280 2.2	W W/^{o}C
T_J, T_{STG}	Operating and Storage Temperature	-55 to 150	^{o}C
	Soldering Temperature for 10 seconds	300 (1.6mm from case)	^{o}C
	Mounting Torque, 8-32 or M3 Screw	10ibf*in (1.1N*m)	

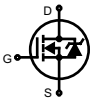
Thermal Characteristics

$R_{\theta JC}$	Thermal Resistance Junction to Case	0.45	^{o}C/W
$R_{\theta CS}$	Thermal Resistance Case to Sink, Flat, Greased Surface	0.24 TYP	^{o}C/W
$R_{\theta JA}$	Thermal Resistance Junction to Ambient	40	^{o}C/W

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
IRFP460A	IRFP460A	TO-247	-	-	-

Electrical Characteristics T_J = 25°C (unless otherwise noted)

Symbol	Parameter	Test Conditions		Min	Typ	Max	Units
Statics							
B_{VDSS}	Drain to Source Breakdown Voltage	I_D = 250μA, V_{GS} = 0V		500	-	-	V
$\Delta B_{VDSS}/\Delta T_J$	Breakdown Voltage Temp. Coefficient	V/˚C Reference to 25°C, ID = 1mA		-	0.61	-	
$r_{DS(ON)}$	Drin to Source On-Resistance	V_{GS} = 10V, I_D = 12A		-	0.17	0.22	Ω
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS} = V_{GS}, I_D = 250μA		2.0	3.3	4.0	V
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS} = 25V	T_C =25°C	-	-	25	μA
		V_{GS} = 0V	T_C = 150°C	-	-	250	
I_{GSS}	Gate to Source Leakage Current	V_{GS} = ±20V		-	-	±100	nA
Dynamics							
g_{fs}	Forward Transconductance	V_{DS} = 50V, I_D = 12A		11	-	-	S
$Q_{g(TOT)}$	Total Gate Charge	V_{GS} = 10V, V_{DS} = 400V, I_D = 20A		-	56	70	nC
Q_{gs}	Gate to Source Gate Charge			-	13	18	nC
Q_{gd}	Gate to Drain “Miller” Charge			-	17	22	nC
$t_{d(ON)}$	Turn-On Delay Time	V_{DD} = 250V, I_D = 20A R_G = 4.3Ω, R_D = 13Ω		-	13	-	ns
t_r	Rise Time			-	8	-	ns
$t_{d(OFF)}$	Turn-Off Delay Time			-	41	-	ns
t_f	Fall Time			-	6	-	ns
C_{ISS}	Input Capacitance	V_{DS} = 25V, V_{GS} = 0V, f = 1MHz		-	3520	-	pF
C_{OSS}	Output Capacitance			-	410	-	pF
C_{RSS}	Reverse Transfer Capacitance			-	21	-	pF
Avalanche Characteristics							
E_{AS}	Single Pulse Avalanche Energy[2]			960	-	-	mJ
I_{AR}	Avalanche Current			-	-	20	A
E_{AR}	Repetitive Avalanche Energy[1]			28	-	-	mJ
Drain-Source Diode Characteristics							
I_S	Continuous Source Current (Body Diode)	MOSFET symbol showing the integral reverse p-n junction diode. 		-	-	20	A
I_{SM}	Pulsed Source Current[1] (Body Diode)			-	-	80	A
V_{SD}	Source to Drain Diode Voltage	I_{SD} = 20A		-	0.86	1.8	V
t_{rr}	Reverse Recovery Time	I_{SD} = 20A, dI_{SD}/dt = 100A/μs		-	560	710	ns
Q_{RR}	Reverse Recovered Charge	I_{SD} = 20A, dI_{SD}/dt = 100A/μs		-	8.0	11	μC

Notes:
1: Repetitive rating; pulse width limited by maximum junction temperature
2: V_{DD} = 50V, Starting T_J = 25˚C, L = 7.0mH, R_G = 25Ω, I_{AS} = 14A
3: I_{SD} </= 14A, di/dt </= 130A/μs, V_{DD} </= $V_{(BR)DSS}$, T_J </= 150˚C
4: Pulse width </= 300μs; duty cycle </= 2%

Typical Characteristics

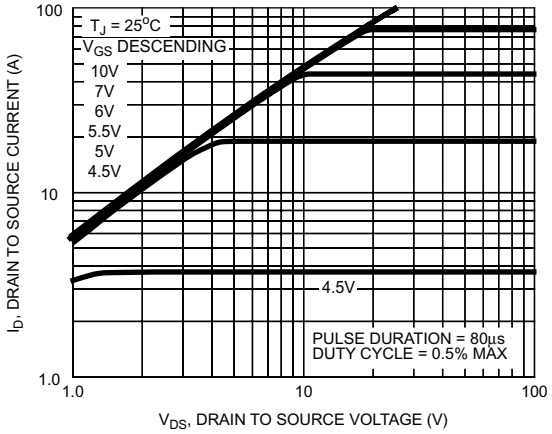


Figure 1. Output Characteristics

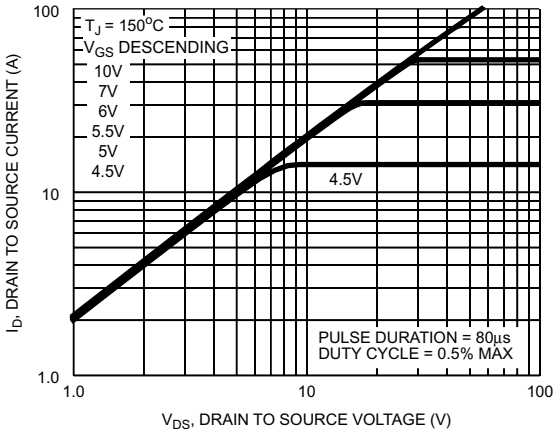


Figure 2. Output Characteristics

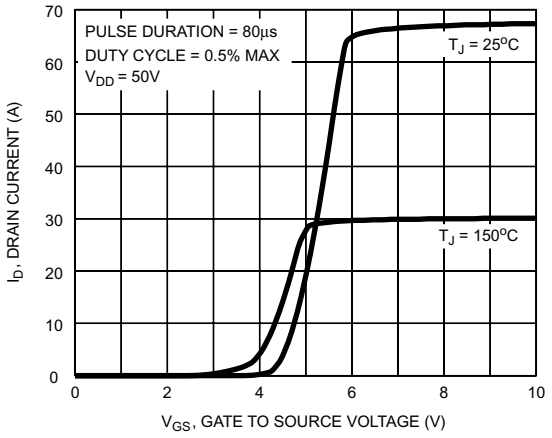


Figure 3. Transfer Characteristics

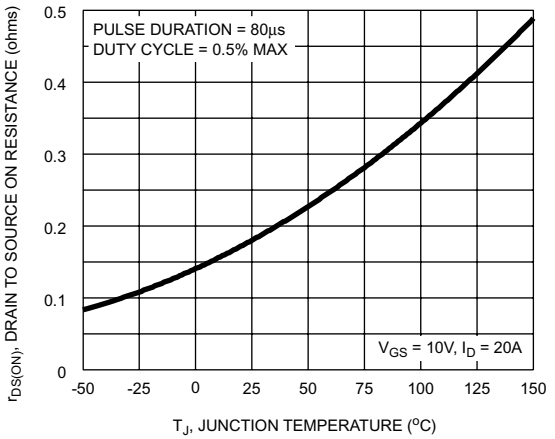


Figure 4. Drain To Source On Resistance vs Junction Temperatrue

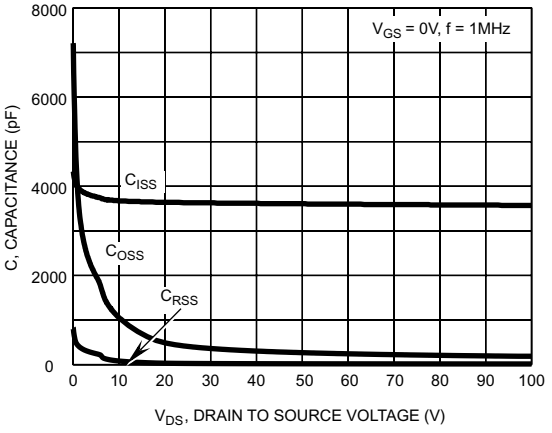


Figure 5. Capacitance vs Drain To Source Voltage

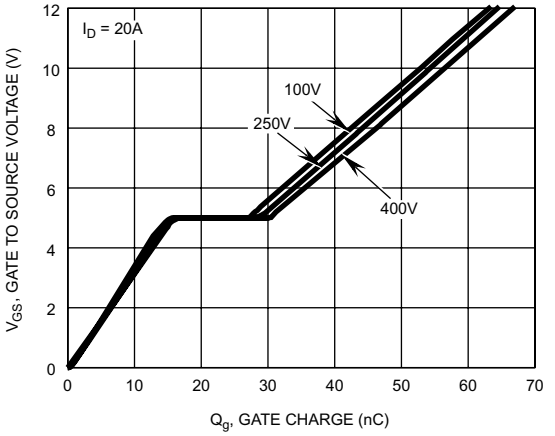


Figure 6. Gate Charge Waveforms For Constant Gate Current

Typical Characteristics (Continued)

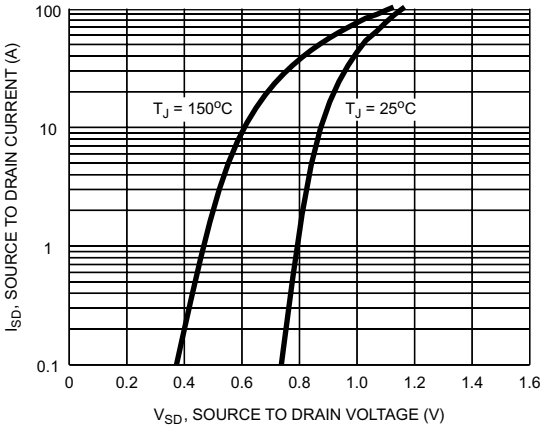


Figure 7. Body Diode Forward Voltage vs Body Diode Current

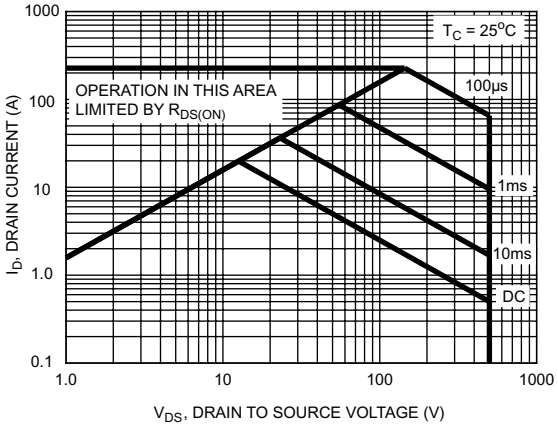


Figure 8. Maximum Safe Operating Area

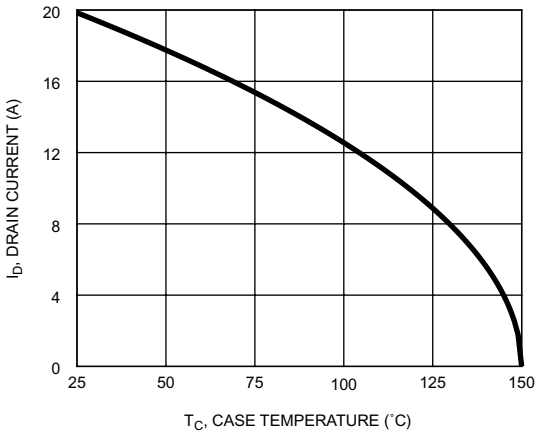


Figure 9. Maximum Drain Current vs Case Temperature

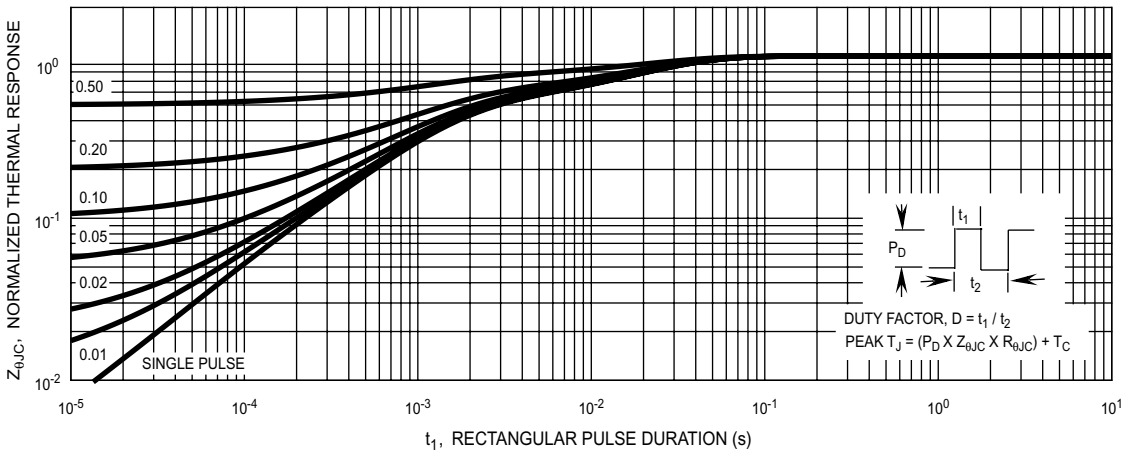


Figure 10. Normalized Transient Thermal Impedance, Junction to Case

Test Circuits and Waveforms

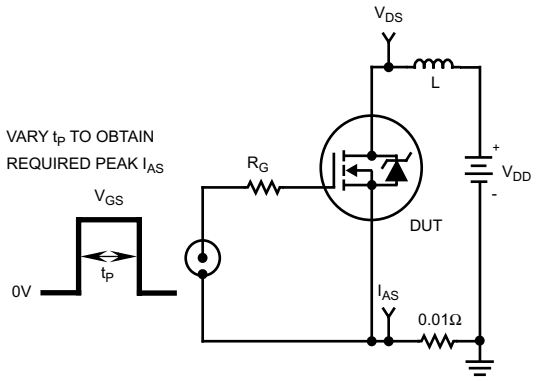


Figure 11. Unclamped Energy Test Circuit

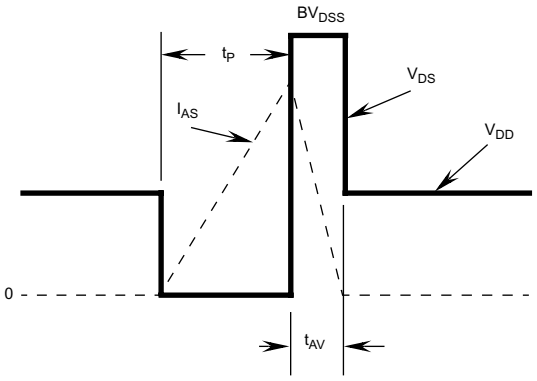


Figure 12. Unclamped Energy Waveforms

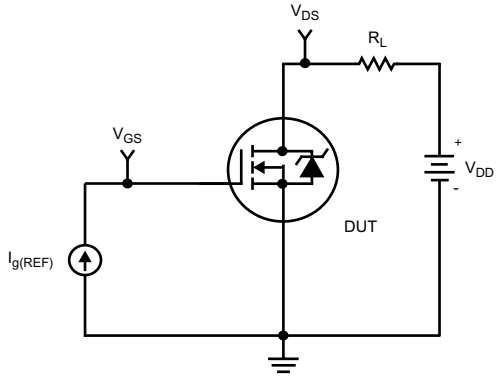


Figure 13. Gate Charge Test Circuit

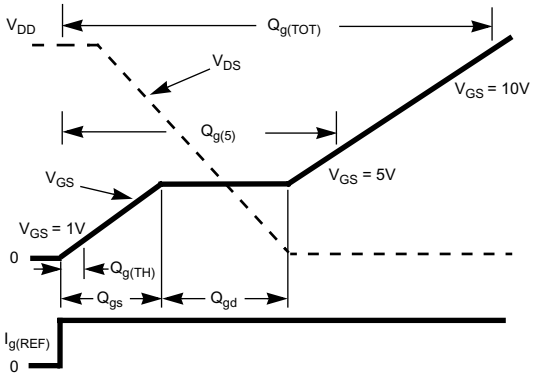


Figure 14. Gate Charge Waveforms

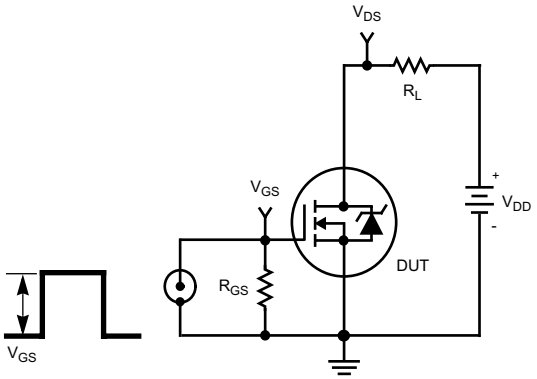


Figure 15. Switching Time Test Circuit

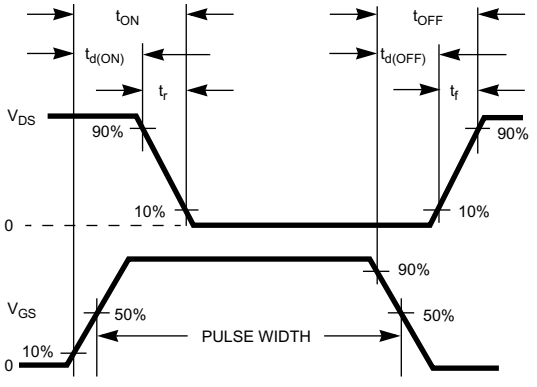


Figure 16. Switching Time Waveform

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