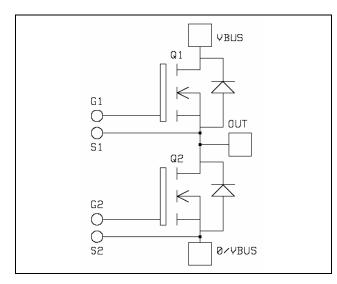
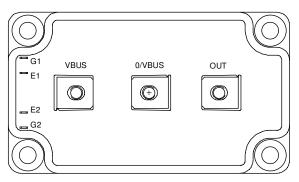


## APTM10AM02F

# Phase leg MOSFET Power Module

$$\begin{split} V_{DSS} &= 100 V \\ R_{DSon} &= 2.25 m\Omega \text{ max } @ \text{ Tj} = 25^{\circ} \text{C} \\ I_D &= 495 \text{A} @ \text{ Tc} = 25^{\circ} \text{C} \end{split}$$





## Application

- Welding converters
- Switched Mode Power Supplies
- Uninterruptible Power Supplies
- Motor control

#### **Features**

- Power MOS V® FREDFETs
  - Low R<sub>DSon</sub>
  - Low input and Miller capacitance
  - Low gate charge
  - Avalanche energy rated
  - Very rugged
- Kelvin source for easy drive
- Very low stray inductance
  - Symmetrical design
  - M5 power connectors

### **Benefits**

- Outstanding performance at high frequency operation
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance

#### **Absolute maximum ratings**

Symbol	Parameter		Max ratings	Unit
$V_{ m DSS}$	Drain - Source Breakdown Voltage		100	V
т	Continuous Drain Current	$T_c = 25^{\circ}C$	495	
$I_D$	Continuous Drain Current	$T_c = 80^{\circ}C$	370	A
$I_{DM}$	Pulsed Drain current		1900	
$V_{GS}$	Gate - Source Voltage		±30	V
$R_{DSon}$	Drain - Source ON Resistance		2.25	$m\Omega$
$P_{D}$	Maximum Power Dissipation $T_c = 25^{\circ}C$		1250	W
$I_{AR}$	Avalanche current (repetitive and non repetitive)		100	A
E <sub>AR</sub>	Repetitive Avalanche Energy		50	mJ
$E_{AS}$	Single Pulse Avalanche Energy		3000	1113

CAUTION: These Devices are sensitive to Electrostatic Discharge. Proper Handing Procedures Should Be Followed.



## APTM10AM02F

## All ratings @ $T_i = 25^{\circ}C$ unless otherwise specified

### **Electrical Characteristics**

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
$BV_{DSS}$	Drain - Source Breakdown Voltage	$V_{GS} = 0V$ , $I_D = 1mA$	100			V
Ţ	Zana Cata Waltaga Duain Cumant	$V_{GS} = 0V, V_{DS} = 100V$ $T_j = 25^{\circ}C$	1		400	^
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{GS} = 0V, V_{DS} = 80V$ $T_j = 125^{\circ}$	С		2000	μA
R <sub>DS(on)</sub>	Drain – Source on Resistance	$V_{GS} = 10V, I_D = 370A$			2.25	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS} = V_{DS}$ , $I_D = 10$ mA	2		4	V
$I_{GSS}$	Gate – Source Leakage Current	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$			±400	nA

**Dynamic Characteristics** 

•	Characteristic	Test Conditions	Min	Тур	Max	Unit
$C_{iss}$	Input Capacitance	$V_{GS} = 0V$		40		
$C_{oss}$	Output Capacitance	$V_{DS} = 25V$		15		nF
$C_{rss}$	Reverse Transfer Capacitance	f = 1MHz		5.5		
$Q_{g}$	Total gate Charge	$V_{GS} = 10V$		1360		
$Q_{gs}$	Gate – Source Charge	$V_{\text{Bus}} = 50V$ $I_{\text{D}} = 400A$		436		nC
$Q_{\mathrm{gd}}$	Gate – Drain Charge			524		
$T_{d(on)}$	Turn-on Delay Time	Resistive Switching $V_{GS} = 15V$		160		
$T_{\rm r}$	Rise Time			240		
$T_{d(off)}$	Turn-off Delay Time	$\begin{array}{c} \begin{array}{c} \begin{array}{c} \\ \\ \end{array} \\ I_D = 400 \end{array} A$		500		ns
$T_{\mathrm{f}}$	Fall Time	$R_G = 0.15 \Omega$		160		

## **Source - Drain diode ratings and characteristics**

Symbol	Characteristic	Test Conditions		Min	Typ	Max	Unit
$I_S$	Continuous Source current		$Tc = 25^{\circ}C$			495	A
	(Body diode)		$Tc = 80^{\circ}C$			370	Λ
$V_{\mathrm{SD}}$	Diode Forward Voltage	$V_{GS} = 0V, I_S = -400A$				1.3	V
dv/dt	Peak Diode Recovery <b>3</b>					5	V/ns
+	Reverse Recovery Time	$I_S = -400A$ $V_R = 50V$	$T_j = 25^{\circ}C$			190	ne
t <sub>rr</sub>	Reverse Recovery Time	$v_R = 30 \text{ V}$ $di_S/dt = 400 \text{A/}\mu\text{s}$	$T_j = 125^{\circ}C$			370	ns
0	Payaraa Pagayary Chargo	$I_{S} = -400A$	$T_j = 25^{\circ}C$		1.6		C
Q <sub>rr</sub>	Reverse Recovery Charge	$V_R = 50V$ $di_S/dt = 400A/\mu s$	$T_j = 125$ °C		6.8		μC

- E<sub>on</sub> includes diode reverse recovery.
- 2 In accordance with JEDEC standard JESD24-1.
- **3** dv/dt numbers reflect the limitations of the circuit rather than the device itself.

 $I_S \le -495A$  di/dt  $\le 400A/\mu s$   $V_R \le 50V$   $T_i \le 150$ °C

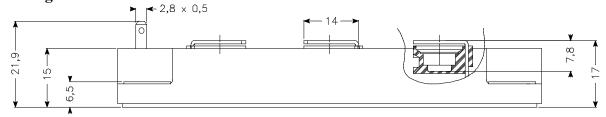


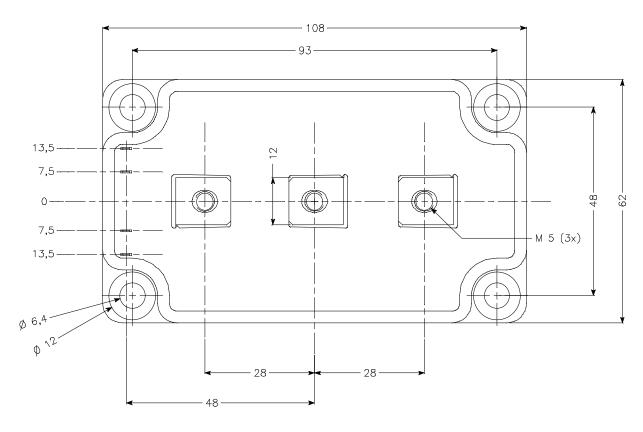
## APTM10AM02F

### Thermal and package characteristics

Symbol	Characteristic			Min	Typ	Max	Unit
$R_{thJC}$	Junction to Case					0.1	°C/W
$V_{\rm ISOL}$	RMS Isolation Voltage, any terminal to case t =1 min,		2500		V		
V ISOL	I Isol<1mA, 50/60Hz	ol<1mA, 50/60Hz					v
$T_{J}$	Operating junction temperature range			-40		150	
$T_{STG}$	Storage Temperature Range			-40		125	°C
$T_{\rm C}$	Operating Case Temperature			-40		100	
Torque	Mounting torque	To heatsink	M6	3		5	N.m
Torque		For terminals	M5	2		3.5	14.111
Wt	Package Weight				280	g	

## Package outline





#### APT reserves the right to change, without notice, the specifications and information contained herein

APT's products are covered by one or more of U.S patents 4,895,810 5,045,903 5,089,434 5,182,234 5,019,522 5,262,336 6,503,786 5,256,583 4,748,103 5,283,202 5,231,474 5,434,095 5,528,058 and foreign patents. U.S and Foreign patents pending. All Rights Reserved.